

**BEST AVAILABLE COPY****Remark**

Applicants respectfully request reconsideration of this application as amended. Claims 1, 14 and 27 have been amended. No Claims have been canceled. Therefore, claims 1-30 are now presented for examination.

**35 U.S.C. §112 Rejection**

Claims 1-30 are rejected under 35 U.S.C. §112, first paragraph, as failing to comply with the written description requirement. The Examiner asserts that there is no support for a specific resistance in the originally filed application. The Examiner, however, does assert that paragraphs 24 and 25 of the specification provide support for a specific impedance for each memory module. The claims have accordingly been amended to recite "impedance" instead of "resistance."

This amendment does not raise any new issues for further consideration or search as the Examiner has clearly reviewed the relevant portions of the specification, Applicant's previous remarks and considered them in light of the prior art. The difference between resistance and impedance are such that if any one of the references discussed impedance in a relevant context, then the Examiner would be obligated to determine whether such a teaching would suggest a specific resistance.

Applicants respectfully request that the amendments be entered as it will place the application in better condition for appeal. Entering the amendment removes the necessity of arguing that "resistance" is taught in the specification so that an appeal may be focused on the anticipation and obviousness rejections.

Notwithstanding this amendment, Applicants maintain that "resistance" is also supported in the specification. First, Applicants point to paragraph 22 which defines Rs,

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a resistance value for the splitter. Second, Applicants assert that resistance and frequency are connected so that in any one steady state condition, there is a specific resistance for each memory module.

**35 U.S.C. §102 Rejection***Morris*

The Examiner has rejected various claims under 35 U.S.C. §102 (e) as being anticipated by Morris, U.S. Patent No. 6,862,185 ("Morris"). This is the same rejection as was provided in the previous rejection. Applicants submit the same response except referring this time to "impedance." In brief, Claim 1 has a bus splitter coupled between the memory modules and the memory controller which is simply not shown or described in Morris.

**35 U.S.C. §103 Rejections***Morris in view of Talbot, Jeddeloh, Freker, Nizar*

The Examiner repeated the rejections under 35 U.S.C. §103 (a) from the previous action. These were responded to in the previous action and the Examiner is respectfully referred to Applicant's previous remarks.

**Conclusion**

Applicants respectfully submit that the rejections have been overcome by the amendment and remark, and that the claims as amended are now in condition for allowance. Accordingly, Applicants respectfully request the rejections be withdrawn and the claims as amended be allowed.

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**BEST AVAILABLE COPY****Invitation for a Telephone Interview**

The Examiner is requested to call the undersigned at (303) 740-1980 if there remains any issue with allowance of the case.

**Request for an Extension of Time**

Applicants respectfully petition for an extension of time to respond to the outstanding Office Action pursuant to 37 C.F.R. § 1.136(a) should one be necessary. Please charge our Deposit Account No. 02-2666 to cover the necessary fee under 37 C.F.R. § 1.17(a) for such an extension.

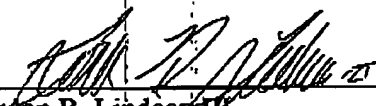
**Charge our Deposit Account**

Please charge any shortage to our Deposit Account No. 02-2666.

Respectfully submitted,

**BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP**

Date: May 5, 2006

  
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